Docket No.: 1614.1210

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Teruhiko KAMIGATA, et al.

Serial No. 10/053,707

Group Art Unit: 2154

Confirmation No. 7916

Filed: January 24, 2002

Examiner: Larry D. Donaghue

For:

INSTRUCTION PROCESSING METHOD FOR VERIFYING BASIC INSTRUCTION

ARRANGEMENT IN VLIW INSTRUCTION FOR VARIABLE LENGTH VLIW

PROCESSOR

RESPONSE

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed April 12, 2006, and having a period for response set to expire on July 12, 2006. A Petition for a two-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to September 12, 2006.

The following remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

98/29/2006 SDENBOB1 00900011 10053707 01 FC:1252 450.00 OP